

Amendments to the Claims

1. (Original) An information processing apparatus for outputting video and audio signals to a home TV set, comprising:

a man-machine interface, a semiconductor memory and an information processor;

said man-machine interface converting into an electrical signal one or plurality of urging force, movement in a space, sound information that are given by a human to said man-machine interface;

said semiconductor memory storing software for driving said information processor;

said software being configured by an operating system, an information processor hardware driver, a man-machine interface driver, an application software engine and application software contents portion;

said operating system administering at least state control of all the tasks included in the present software, task scheduling, shared resource control between tasks, and interrupt control;

said information processor hardware driver being to efficiently handle hardware resource in said information processor and configured by a driver program and driver data;

said driver program including totally one or more tasks and subroutines, and being to be utilized in function according to task execution or a subroutine call from said application software engine;

said driver data being a set of data to be handled by said driver program;

said man-machine interface driver being to efficiently deliver said electrical signal from said man-machine interface to said application software engine, and including totally one or more tasks and subroutines, and to be utilized in function according to task execution of a subroutine call from said application software engine;

said application software engine being to perform a process relied upon an application kind among regular processes required by said application software contents portion and including totally one or more tasks and subroutines, and utilized in function task execution or subroutine call from an application software contents program;

said application software contents portion being configured by an application software contents program and application software contents data;

said application software contents program being a program code for a particular process to achieve an objective of said present information processing apparatus and including one or more tasks;

said application software contents data being a set of data to be handled by said application software contents program or said application software engine; and

said information processor being to perform an operation process based on an electrical signal from said man-machine interface and software stored in said semiconductor memory, and produce image information and sound information.

2. (Currently Amended) An information processing apparatus according to claim 1, wherein said information processor having a central processor, a graphics processor and a sound processor, said central processor, graphics processor and sound processor being connected to a common bus to which said semiconductor memory is connected;

said central processor, said graphic processor and said sound processor sharing a memory space in which said semiconductor memory is allocated, and sharing said semiconductor memory as bus masters for accessing said semiconductor memory actively;

said central processor controlling said graphics processor and said sound processor based on said electrical information from said man-machine interface and a program code in said software;

said graphics processor having means to generate image information, and
said sound processor having means to generate sound information.

3. (Original) An information processing apparatus for outputting video and audio signals to a home TV set, comprising:

a man-machine interface, a semiconductor memory and an information processor;

said man-machine interface converting into an electrical signal one or a plurality of urging force, in-space movement, sound information that are given by a human to said present man-machine interface;

said semiconductor memory storing software for driving said information processor;

said software being configured by an operating system, an information processor hardware driver, a man-machine interface driver, a script language interpreter and an application software contents portion;

said operating system administering at least status control of all the tasks included in the present software, task scheduling, shared resource control between tasks, and interrupt control;

said information processor hardware driver being to efficiently handle a hardware resource in said information processor and configured by a driver program and driver data;

said driver program including totally one or more tasks and subroutines and being to be utilized in function according to task execution or subroutine call from said script language interpreter;

said driver data being a set of data to be handled by said driver program;

said man-machine interface driver being to efficiently deliver said electrical information from said man-machine interface to said script language interpreter and including totally one or more tasks and subroutines, and utilized in function according to task execution or a subroutine call from said script language interpreter;

said script language interpreter being to sequentially interrupt a script language source code to produce and execute an object code interpretable by said information processor;

said application software contents portion being configured by a script language source code and application software contents data;

said script language source code being a program for a particular process to achieve an objective of said present information processing apparatus;

said application software contents data being a set of data to be handled by said script language source code or said script language interpreter; and

said information processor being to perform an operation process based on an electrical signal from said man-machine interface and software stored in said semiconductor memory, and producing image information and sound information.

4. (Currently Amended) An information processing apparatus according to claim 3, wherein said information processor has a central processor, a graphics processor and a sound processor, said central processor, graphics processor and sound processor being connected to a common bus to which said semiconductor memory is connected;

said central processor, said graphics processor and said sound processor sharing a memory space in which said semiconductor memory is allocated, and sharing said semiconductor memory as bus masters for accessing said semiconductor memory actively;

said central processor controlling said graphic processor and said sound processor based on said electrical information from said man-machine interface and a program code in said software;

said graphics processor having means to generate image information; and
said sound processor having means to generate sound information.

5. (Original) An information processing apparatus according to claim 1 or 2, further comprising communication means capable of transmitting and receiving data and/or a program through a general communication line, wherein

said information processor is to perform an operation process based on an electrical signal from said man-machine interface, said software stored in said semiconductor memory and said data and/or program obtained from said communication means.

6. (Original) An information processing apparatus according to claim 3 or 4, further comprising communication means capable of transmitting and receiving data and/or a program through a general communication line, wherein

said information processor is to perform an operation process based on an electrical signal from said man-machine interface, said software stored in said semiconductor memory data and/or a program obtained from said communication means.

7. (Original) An information processing apparatus according to any of claims 1, 2 and 5, wherein said man-machine interface means, said semiconductor memory and said information processor are incorporated in a single apparatus.

8. (Original) An information processing apparatus according to any of claims 3, 4 and 6, wherein said man-machine interface means, said semiconductor memory and said information processor are incorporated in a single apparatus.